



29	14	(asynchronous\$2 near3 latch\$3) & ((float\$ ) near10 (detect\$ sense\$1 sensing))	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/08 09:53
-	2	"20030051085" .pn.	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:48
-	129	cable near3 ide	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:48
-	15953	latch\$3 near3 (sense senses sensing detect\$3)	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 18:16
-	3	(latch\$3 near3 (sense senses sensing detect\$3)) & (cable near3 ide)	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:53
-	83	(40pin "40-pin" "40 pin") near3 cable\$1	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:54
-	1	((latch\$3 near3 (sense senses sensing detect\$3)) & (cable near3 ide)) & ((40pin "40-pin" "40 pin") near3 cable\$1)	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:54
-	20	((40pin "40-pin" "40 pin") near3 cable\$1 & (cable near3 ide)	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:54
-	3933	(40pin "40-pin" "40 pin")	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:54
-	1624	(80pin "80-pin" "80 pin" (pin\$1 near2 6467002.PN.))	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:55
-	84	((40pin "40-pin" "40 pin") & ((80pin "80-pin" "80 pin" (pin\$1 near2 6467002.PN.)))	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:55
-	1	((40pin "40-pin" "40 pin") & ((80pin "80-pin" "80 pin" (pin\$1 near2 6467002.PN.))) & (cable near3 ide)	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:55
-	6	((40pin "40-pin" "40 pin") & ((80pin "80-pin" "80 pin" (pin\$1 near2 6467002.PN.))) & ide	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 17:57
-	7	((40pin "40-pin" "40 pin") & ((80pin "80-pin" "80 pin" (pin\$1 near2 6467002.PN.))) & ata	USPAT; US-FGPUB; EPO: JPO; DERWENT; 18M_TDB	2004/04/07 18:05

-	220	ground adj1 signal adj1 ground	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:45
-	67	cable & (ground adj1 signal adj1 ground)	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:06
-	2	ata & (cable & (ground adj1 signal adj1 ground))	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:06
-	10	clock#3 & (cable & (ground adj1 signal adj1 ground))	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:14
-	122	439/928.1.cc1s.	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:14
-	11	ide & 439/928.1.cc1s.	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:14
-	0	(latch#3 near3 (sense senses sensing detect#3)) & 439/928.1.cc1s.	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:15
-	0	(latch#3 near3 (sense senses sensing detect#3)) & ((latch#3 near3 (sense senses sensing detect#3)) & 439/928.1.cc1s.)	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:15
-	27	439/928.1.cc1s. & (sense senses sensing detect#3)	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:16
-	21735	cable near5 (sense senses sensing detect#3)	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:44
-	7	(ground adj1 signal adj1 ground) & (cable near5 (sense senses sensing detect#3))	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:44
-	37	ground near1 signal near5 ribbon	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:46
-	0	(ground near1 signal near5 ribbon) & (latch#3 near3 (sense senses sensing detect#3))	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:46
-	3	(ground near1 signal near5 ribbon) & (cable near5 (sense senses sensing detect#3))	USP4T; US-PCRB; EPO; JPO; DERMENT; TBM TDB	2004/04/07 18:51

-	41232	ground near3 (wire\$1 conductor\$1)	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 18:51
-	24101	(ribbon flat) near3 cable\$1	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 20:05
-	1108	(ground near3 (wire\$1 conductor\$1)) & ((ribbon flat) near3 cable\$1)	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 18:52
-	59	(cable near5 (sense senses sensing detect\$3)) & ((ground near3 (wire\$1 conductor\$1)) & ((ribbon flat) near3 cable\$1))	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 18:55
-	396	324/66.ccls.	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 18:56
-	14	((ribbon flat) near3 cable\$1) & 324/66.ccls.	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 18:59
-	0	(ground adj1 signal adj1 ground) & 324/66.ccls.	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 18:59
-	51	(ground near3 (wire\$1 conductor\$1)) & 324/66.ccls.	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 20:04
-	108	floating near5 latched	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 20:05
-	2	((ribbon flat) near3 cable\$1) & (floating near5 latched)	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 20:05
-	761	floating near5 latch\$2	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 20:06
-	1	(cable near5 (sense senses sensing detect\$3)) & (floating near5 latch\$2)	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 20:06
-	58	floating near3 (conductor\$1 wire\$1) near5 (detect\$3 sense senses sensing)	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 20:08
-	3	cable & (floating near3 (conductor\$1 wire\$1) near5 (detect\$3 sense senses sensing))	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 20:09

-	25	ata & pdiag	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/07 20:10
-	4	ata & pdiag & cbiid	USPAT; US-PPUB; EPO; JFO; DERMENT; IBM_TDB	2004/04/08 06:26

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CONTROLS CAMAC 145 VACUUM READBACK INTERFACE

... the low to high transition of the signal triggers the flip ... 5.5 The MC68881 Floating  
Point Coprocessor (FPC). ... the 10 MHz carrier of the Tevatron clock is detected ...

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Processor Clock Speed MHz ... Reset initialises the transputer, triggers the memory ...  
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... dev 01 SIS5513: not 100% native mode: will probe irq's later SIS5513: Chipset Core ATA-66, SIS620 SIS5513: Primary ATA-66, Secondary ATA-33 **Cable Detect** ide0: BM ...  
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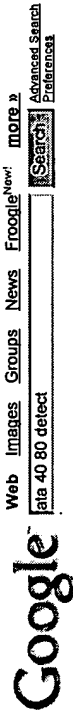
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## The Hardware Site. What is Ultra ATA/66 ?

... 66, the chipset pin count remains the same at 40. ... such that the PC and the Ultra ATA/66 compliant ... both detect the presence of the required 80-conductor cable. ... [www.hardwarepage.nl/ultra66.html](http://www.hardwarepage.nl/ultra66.html) - 7k - Cached - Similar pages

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... For some reason the ATA people couldn't get this ... chipsets where there is a 'cable detect scratch register', where the BIOS writes the 30/40 pin cable ...  
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